

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

15983US01

In the Application of:
Rebecca Yuan

Serial No.: 10/035,567

Filed: October 22, 2001

Title: SYSTEM AND METHOD FOR DC
OFFSET COMPENSATION AND BIT
SYNCHRONIZATION

Group Art Unit: 2681

Examiner: Not Yet Assigned

Confirmation Number: 3737

VIA FACSIMILE

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STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is submitted as an inquiry as to the status of the above-identified patent application.

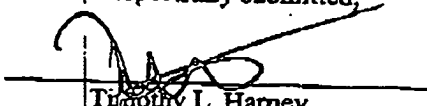
A Notice of Publication of Application, dated April 24, 2003, was received from the Patent and Trademark Office on May 5, 2003. No further communication has been received from the Patent and Trademark Office indicating any further action on this application since that time.

A Power of Attorney by Assignee of Entire Interest, Revocation of Prior Powers and Change of Correspondence Address with Exhibit A attached thereto were filed on September 9, 2004.

Kindly advise the undersigned of the present status of this application.

Respectfully submitted,

Dated: September 21, 2004


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